

August 1986 Revised March 2000

DM74LS38

Quad 2-Input NAND Buffer with Open-Collector Outputs

General Description

This device contains four independent gates, each of which performs the logic NAND function. The open-collector outputs require external pull-up resistors for proper logical operation.

Pull-Up Resistor Equations

$$R_{MAX} = \frac{V_{CC} \left(Min\right) - V_{OH}}{N_1 \left(I_{OH}\right) + N_2 \left(I_{IH}\right)}$$

$$R_{MIN} = \frac{V_{CC} (Max) - V_{OL}}{I_{OL} - N_3 (I_{IL})}$$

Where:

 $N_1 \ (I_{OH}) = total \ maximum \ output \ high \ current$ for all outputs tied to pull-up resistor

 N_2 (I_{IH}) = total maximum input high current for

all inputs tied to pull-up resistor

 $N_{3}\ (I_{IL})=total\ maximum\ input\ low\ current\ for$

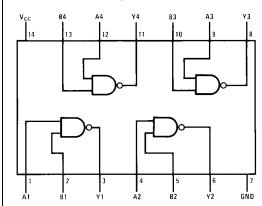
all inputs tied to pull-up resistor

Ordering Code:

	Order Number	Package Number	Package Description		
	DM74LS38M	M14A	14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-120, 0.150 Narrow		
	DM74LS38SJ	M14D	14-Lead Small Outline Package (SOP), EIAJ TYPE II, 5.3mm Wide		
	DM74LS38N	N14A	14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide		

Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering code.

Connection Diagram



Function Table

$Y = \overline{AB}$						
	Inp	Output				
	Α	В	Y			
	L	L	Н			
	L	Н	Н			
	Н	L	Н			
	Н	Н	L			

H = HIGH Logic Level L = LOW Logic Level

Absolute Maximum Ratings(Note 1)

 $\begin{array}{ccc} \text{Supply Voltage} & 7V \\ \text{Input Voltage} & 7V \\ \text{Output Voltage} & 7V \\ \text{Operating Free Air Temperature Range} & 0^{\circ}\text{C to } +70^{\circ}\text{C} \\ \end{array}$

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	Min	Nom	Max	Units
V _{CC}	Supply Voltage	4.75	5	5.25	V
V _{IH}	HIGH Level Input Voltage	2			V
V _{IL}	LOW Level Input Voltage			0.8	V
V _{OH}	HIGH Level Output Voltage			5.5	V
I _{OL}	LOW Level Output Current			24	mA
T _A	Free Air Operating Temperature	0		70	°C

 $-65^{\circ}C$ to $+150^{\circ}C$

Electrical Characteristics

Storage Temperature Range

over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ (Note 2)	Max	Units
VI	Input Clamp Voltage	$V_{CC} = Min, I_I = -18 \text{ mA}$			-1.5	V
I _{CEX}	HIGH Level	$V_{CC} = Min, V_O = 5.5V$			250	μА
	Output Current	V _{IL} = Max				
V _{OL}	LOW Level	V _{CC} = Min, I _{OL} = Max		0.35	0.5	V
	Output Voltage	V _{IH} = Min		0.55	0.5	
		I _{OL} = 12 mA, V _{CC} = Min		0.25	0.4	
II	Input Current @ Max Input Voltage	$V_{CC} = Max, V_I = 7V$			0.1	mA
I _{IH}	HIGH Level Input Current	$V_{CC} = Max, V_I = 2.7V$			20	μΑ
I _{IL}	LOW Level Input Current	$V_{CC} = Max, V_I = 0.4V$			-0.36	mA
I _{CCH}	Supply Current with Outputs HIGH	V _{CC} = Max		0.9	2	mA
I _{CCL}	Supply Current with Outputs LOW	V _{CC} = Max		6	12	mA

Note 2: All typicals are at V_{CC} = 5V, T_A = 25°C.

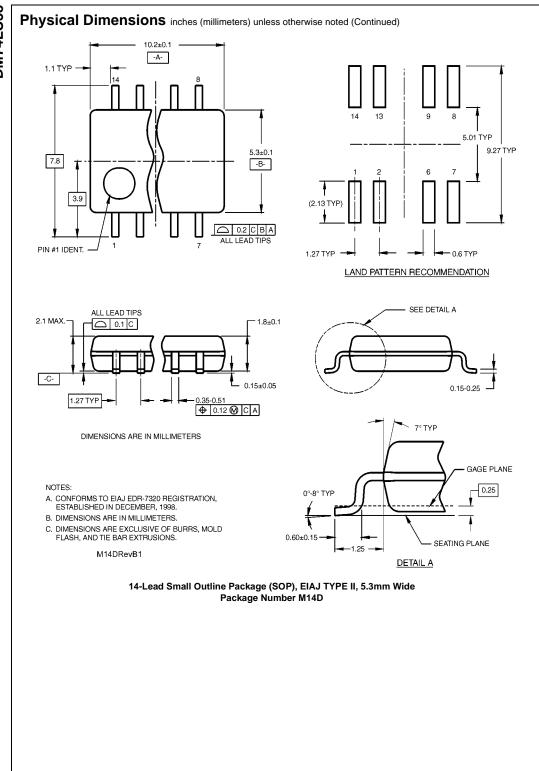
Switching Characteristics

at $V_{CC} = 5V$ and $T_A = 25^{\circ}C$

	Parameter	$R_L = 667\Omega$				
Symbol		C _L = 45 pF		C _L = 150 pF		Units
		Min	Max	Min	Max	
t _{PLH}	Propagation Delay Time LOW-to-HIGH Level Output		22		48	ns
t _{PHL}	Propagation Delay Time HIGH-to-LOW Level Output		22		29	ns

Physical Dimensions inches (millimeters) unless otherwise noted $\frac{0.335 - 0.344}{(8.509 - 8.738)}$ $\frac{0.228 - 0.244}{(5.791 - 6.198)}$ LEAD NO. 1 IDENT $\frac{0.150 - 0.157}{(3.810 - 3.988)}$ $\frac{0.053 - 0.069}{(1.346 - 1.753)}$ $\frac{0.010 - 0.020}{(0.254 - 0.508)}$ 8° MAX TYP ALL LEADS $\frac{0.004 - 0.010}{(0.102 - 0.254)}$ SEATING PLANE 0.014 0.008 - 0.010 (0.203 - 0.254) TYP ALL LEADS $\frac{0.014 - 0.020}{(0.356 - 0.508)} \text{ TYP}$ 0.050 (1.270) TYP 0.016 - 0.050 (0.406 - 1.270) TYP ALL LEADS 0.004 (0.102) ALL LEAD TIPS 0.008 (0.203) TYP M14A (REV H)

14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-120, 0.150 Narrow Package Number M14A



Physical Dimensions inches (millimeters) unless otherwise noted (Continued) 0.740 - 0.770(18.80 - 19.56)0.090 (2.286) 14 13 12 11 10 9 8 14 13 12 0.250 ± 0.010 PIN NO. 1 IDENT PIN NO. 1 IDENT 1 2 3 4 5 6 7 1 2 3 $\frac{0.092}{(2.337)}$ DIA 0.030 MAX (0.762) DEPTH OPTION 1 OPTION 02 $\frac{0.135 \pm 0.005}{(3.429 \pm 0.127)}$ 0.300 - 0.320 $\overline{(7.620 - 8.128)}$ 0.065 $\frac{0.145 - 0.200}{(3.683 - 5.080)}$ 0.060 4° TYP Optional (1.524) (1.651) $\frac{0.008 - 0.016}{(0.203 - 0.406)}$ TYP 0.020 (0.508) 0.125 - 0.150 0.075 ± 0.015 $\overline{(3.175 - 3.810)}$ 0.280 (1.905 ± 0.381) (7.112) MIN 0.014 - 0.0230.100 ± 0.010 (2.540 ± 0.254) TYP (0.356 - 0.584)

14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide Package Number N14A

 $\frac{0.050\pm0.010}{(1.270-0.254)}$ TYP

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 $0.325 + 0.040 \\ -0.015 \\ \hline (8.255 + 1.016) \\ -0.381)$

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N144 (REV.E)

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